

S/N 10/810,035

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Albert S. Weiner	Examiner: Thinh T. Nguyen
Serial No.:	10/810,035	Group Art Unit: 2818
Filed:	March 26, 2004	Docket No.: 2800.450US1
Title:	Non-volatile transistor memory array incorporating read-only elements with single mask set	

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on May 27, 2008. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office Action, thereby moving the deadline for response from August 27, 2008 to September 27, 2008.